

Abstract

A method and apparatus for spread spectrum clock generation is provided.

Modulation of the clock signal may be accomplished with an $N/N-1$ clock divider. The $N/N-1$ clock divider is configured to divide the clock signal by N or $N-1$, depending on the carry output signal of an accumulator circuit. The accumulator circuit is configured to provide the carry output signal in response to a modulating waveform signal. The modulating waveform signal may be a triangle wave, a sinusoidal wave, another waveform appropriate for spread-spectrum clock generation, and the like.

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